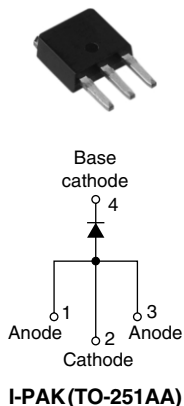


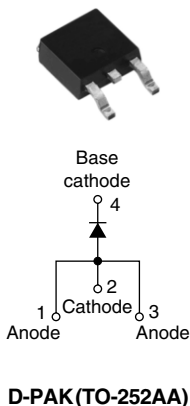
High Performance Schottky Generation 5.0, 10 A

10UT10



I-PAK(TO-251AA)

10WT10FN



D-PAK(TO-252AA)

FEATURES

- 175 °C high performance Schottky diode
- Very low forward voltage drop
- Extremely low reverse leakage
- Optimized V_F vs. I_F trade off for high efficiency
- Increased ruggedness for reverse avalanche capability
- RBSOA available
- Negligible switching losses
- Submicron trench technology
- Compliant to RoHS directive 2002/95/EC
- AEC-Q101 qualified



RoHS
COMPLIANT

APPLICATIONS

- High efficiency SMPS
- Automotive
- High frequency switching
- Output rectification
- Reverse battery protection
- Freewheeling
- Dc-to-dc systems
- Increased power density systems

PRODUCT SUMMARY

$I_{F(AV)}$	10 A
V_{RRM}	100 V
Maximum V_F at 10 A at 125 °C	0.66 V

MAJOR RATINGS AND CHARACTERISTICS

SYMBOL	CHARACTERISTICS	VALUES	UNITS
V_{RRM}		100	V
V_F	10 Apk, $T_J = 125$ °C (typical)	0.615	V
T_J	Range	- 55 to 175	°C

VOLTAGE RATINGS

PARAMETER	SYMBOL	TEST CONDITIONS	10UT10 10WT10FN	UNITS
Maximum DC reverse voltage	V_R	$T_J = 25$ °C	100	V

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum average forward current	I _{F(AV)}	50 % duty cycle at T _C = 159 °C, rectangular waveform		10	A
Maximum peak one cycle non-repetitive surge current	I _{FSM}	5 μs sine or 3 μs rect. pulse	Following any rated load condition and with rated V _{RRM} applied	610	A
		10 ms sine or 6 ms rect. pulse		110	
Non-repetitive avalanche energy	E _{AS}	T _J = 25 °C, I _{AS} = 3 A, L = 12 mH		54	mJ
Repetitive avalanche current	I _{AR}	Limited by frequency of operation and time pulse duration so that T _J < T _J max. I _{AS} at T _J max. as a function of time pulse (see fig. 8)		I _{AS} at T _J max.	A

Note

(1) Measured connecting 2 anode pins

ELECTRICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS		TYP.	MAX.	UNITS
Forward voltage drop	$V_{\text{FM}}^{(1)(2)}$	5 A	$T_{\text{J}} = 25\text{ }^{\circ}\text{C}$	0.630	-	V
		10 A		0.735	0.810	
		20 A		0.840	0.890	
		5 A	$T_{\text{J}} = 125\text{ }^{\circ}\text{C}$	0.530	-	
		10 A		0.615	0.660	
		20 A		0.730	0.770	
Reverse leakage current	$I_{\text{RM}}^{(1)}$	$T_{\text{J}} = 25\text{ }^{\circ}\text{C}$	$V_{\text{R}} = \text{Rated } V_{\text{R}}$	-	50	μA
		$T_{\text{J}} = 125\text{ }^{\circ}\text{C}$		-	4	mA
Junction capacitance	C_{T}	$V_{\text{R}} = 5\text{ V}_{\text{DC}}$ (test signal range 100 kHz to 1 MHz), $25\text{ }^{\circ}\text{C}$		400	-	pF
Series inductance	L_{S}	Measured lead to lead 5 mm from package body		8.0	-	nH
Maximum voltage rate of change	dV/dt	Rated V_{R}		-	10 000	V/ μs

Notes(1) Pulse width < 300 μs , duty cycle < 2 %

(2) Only 1 anode pin connected

THERMAL - MECHANICAL SPECIFICATIONS				
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum junction and storage temperature range	T_J, T_{Stg}		- 55 to 175	$^{\circ}\text{C}$
Maximum thermal resistance, junction to case	R_{thJC}	DC operation	2	$^{\circ}\text{C/W}$
Typical thermal resistance, case to heatsink	R_{thCS}		0.3	
Approximate weight			0.3	g
			0.01	oz.
Marking device		Case style I-PAK	10UT10	
		Case style D-PAK	10WT10FN	

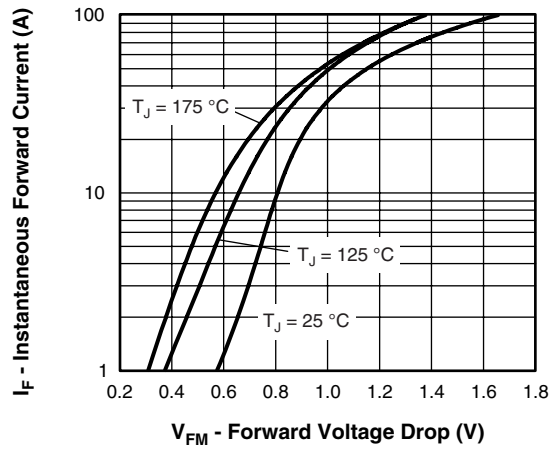


Fig. 1 - Maximum Forward Voltage Drop Characteristics

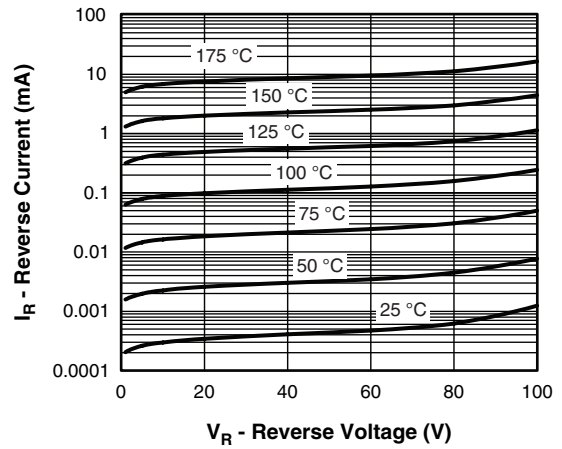


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

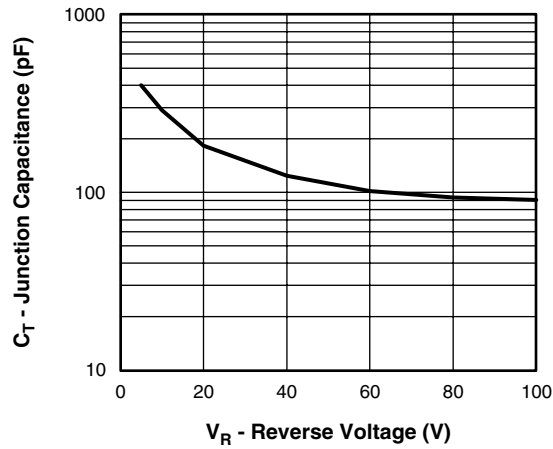


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

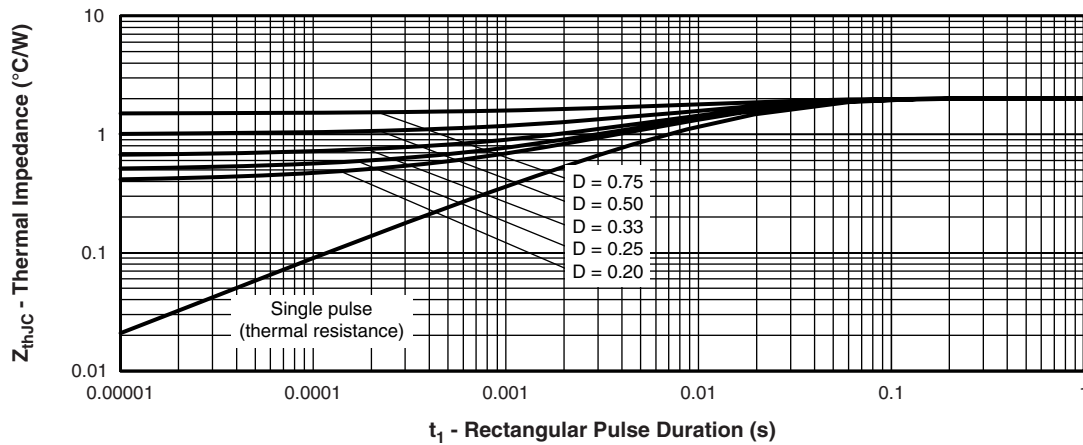


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics

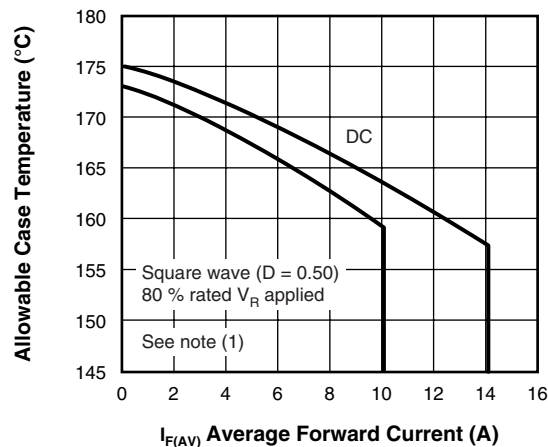


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current

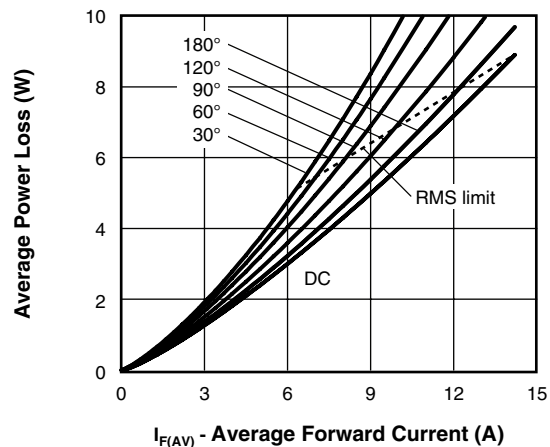


Fig. 6 - Forward Power Loss Characteristics

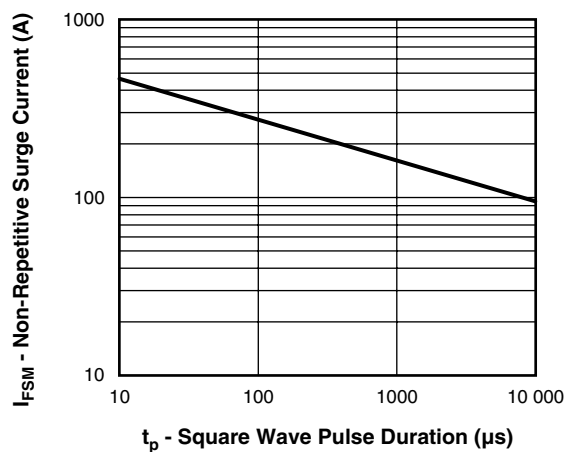


Fig. 7 - Maximum Non-Repetitive Surge Current

Note

- (1) Formula used: $T_C = T_J - (P_d + P_{d_{REV}}) \times R_{thJC}$;
 P_d = Forward power loss = $I_{F(AV)} \times V_{FM}$ at $(I_{F(AV)}/D)$ (see fig. 6);
 $P_{d_{REV}}$ = Inverse power loss = $V_{R1} \times I_R (1 - D)$; I_R at $V_{R1} = 80\%$ rated V_R

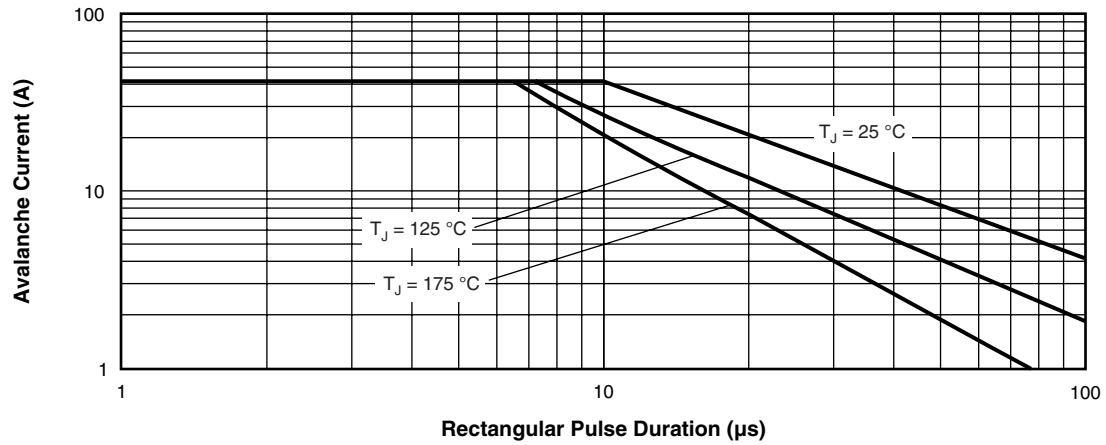


Fig. 8 - Reverse Bias Safe Operating Area (Avalanche Current vs. Rectangular Pulse Duration)

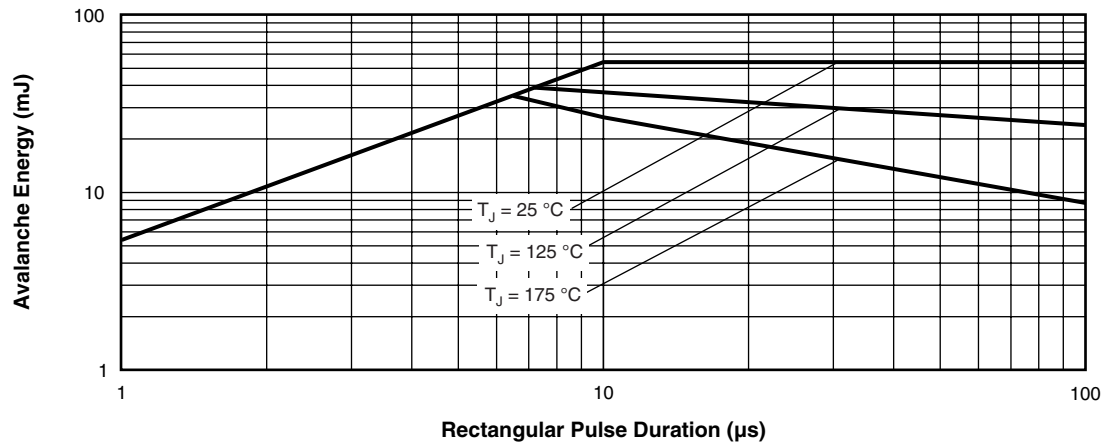


Fig. 9 - Reverse Bias Safe Operating Area (Avalanche Energy vs. Rectangular Pulse Duration)

ORDERING INFORMATION TABLE

Device code	10	U	T	10	FN	TRL
	①	②	③	④	⑤	⑥
	1	-	-	-	-	-
	2	-	-	-	-	-
	3	-	-	-	-	-
	4	-	-	-	-	-
	5	-	-	-	-	-
	6	-	-	-	-	-

1 - Current rating (10 A)
2 - Package:
 • U = I-PAK
 • W = D-PAK
3 - T = Trench
4 - Voltage code (100 V)
5 - TO-252AA (D-PAK)
6 - D-PAK, I-PAK: None = Tube (75 pieces)
 D-PAK only:
 • TR = Tape and reel
 • TRL = Tape and reel (left oriented)
 • TRR = Tape and reel (right oriented)

LINKS TO RELATED DOCUMENTS	
Dimensions	www.vishay.com/doc?95024
Part marking information	www.vishay.com/doc?95025
Packaging information	www.vishay.com/doc?95033
SPICE model	www.vishay.com/doc?95026



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