

ABDF SERIES

STRATUM-III TCXO / VCTCXO

Shrink Footprint
 Improve Performance
 Simplify Design

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The ABDF series of Stratum-III TCXO/VCTCXO integrate a precision ± 280 ppb TCXO with precision low noise output buffer and divider improving signal to noise ratio, reduced BER with a greatly reduced PCB area and simplified design in high performance synchronization applications.

FEATURES

- Phase and Frequency Coherent Dual Output
- F0 and F0÷2 With Integrated Precision Fanout Buffers
- Meets ± 280 Ppb Stratum III Stability Requirements
- Excellent ± 1 Ppm Aging with ± 4.6 Ppm Guaranteed Lifetime Accuracy
- 5 to 52Mhz Standard and Custom Frequencies available
- TCXO or VCTCXO Functionality Available
- ± 5 ppm Minimum Pull with 0.5% Typ Linearity (VCTCXO Only)
- LVC MOS Outputs
- 10 Pin Package (9.0 X 14.0 X 3.0mm)
- 40°C To 85°C Operation

APPLICATIONS

- Communications and Networking
- Synchronization
- PLL with Holdover
- Geolocation, RTLS, GPS
- Synchronous Ethernet
- IEEE1588
- Instrumentation, Test and Measurement
- Femtocells, Picocells (BTS)
- Oil and Gas Exploration

DIGITAL FPGA PLL WITH PRECISION STRATUM-III HOLDOVER

