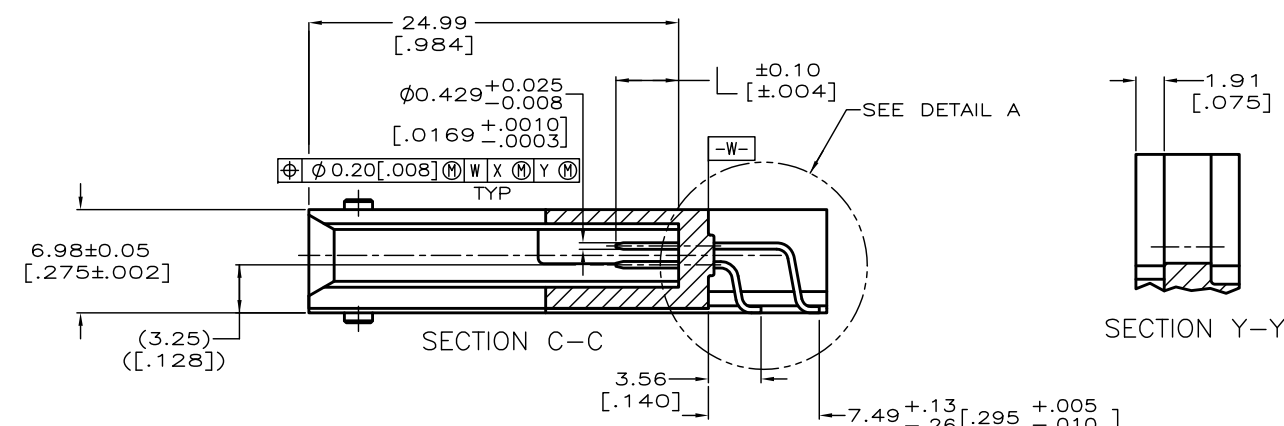
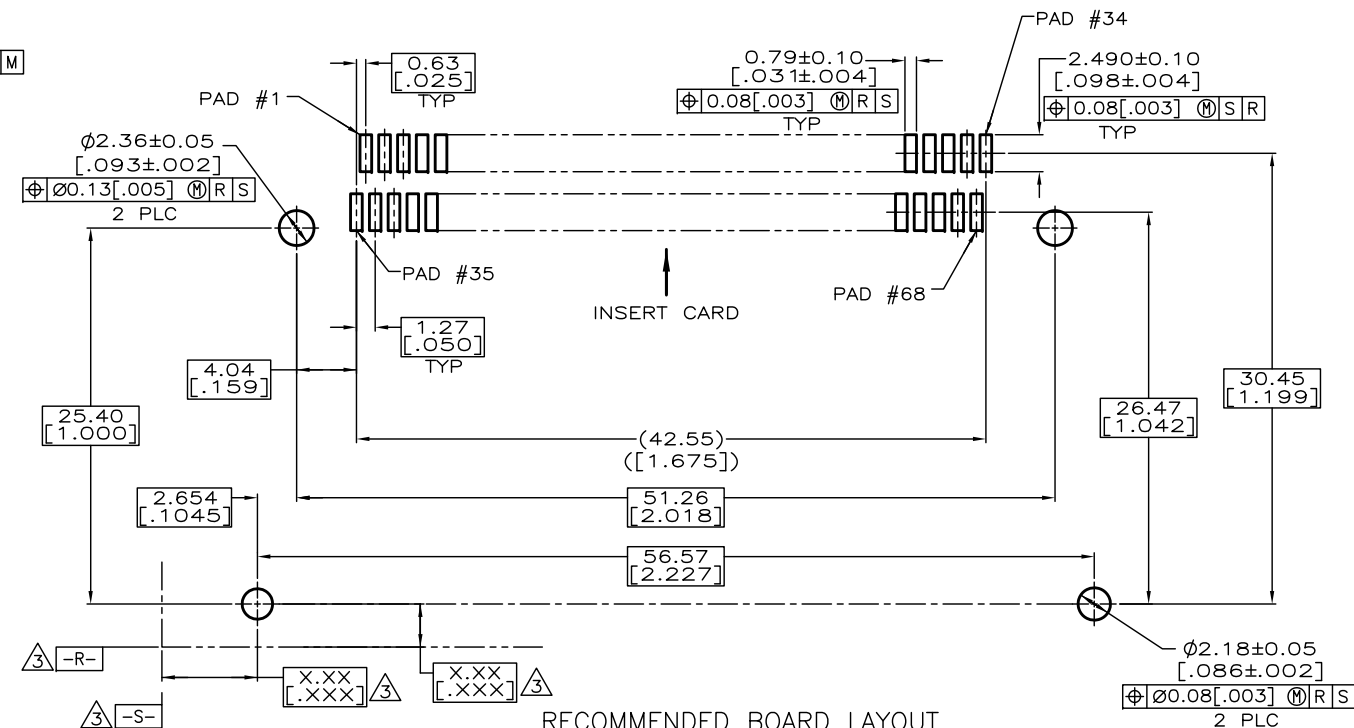
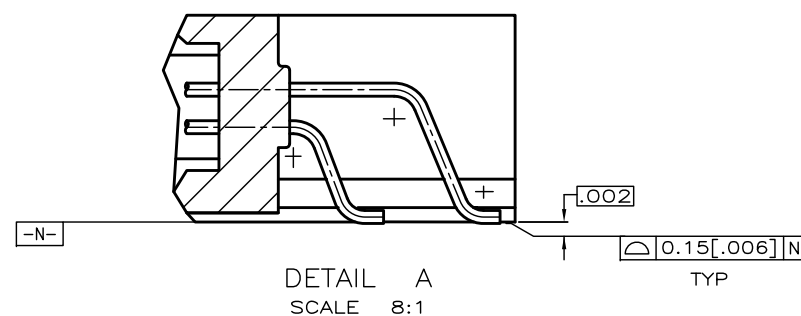




- 13 OBSOLETE PARTS: OBSOLETE CIS STREAMLINING PER D.RENAUD/D.SINISI



|          |                                                                                       |                                                                                             |                     |                   |               |                      |
|----------|---------------------------------------------------------------------------------------|---------------------------------------------------------------------------------------------|---------------------|-------------------|---------------|----------------------|
| OBSOLETE |  | TAPE AND REEL                                                                               | 1,17,34<br>35,51,68 | ALL<br>OTHER PINS | 36,67         | 5535651-6            |
|          |  | TAPE AND REEL                                                                               | 1,17,34<br>35,51,68 | ALL<br>OTHER PINS | 36,67         | <del>5535651-5</del> |
|          |  |  TRAYS | 1,17,34<br>35,51,68 | ALL<br>OTHER PINS | 36,67         | <del>5535651-4</del> |
|          |  | TAPE AND REEL                                                                               | 1,17,34<br>35,51,68 | ALL<br>OTHER PINS | 36,67         | <del>5535651-3</del> |
|          |  |  TRAYS | 1,17,34<br>35,51,68 | ALL<br>OTHER PINS | 36,67         | <del>5535651-2</del> |
|          |  |  TRAYS | 1,17,34<br>35,51,68 | ALL<br>OTHER PINS | 36,67         | 5535651-1            |
|          | VOLTAGE KEYING<br>& IDENTIFICATION                                                    | PACKAGING                                                                                   | 5.0<br>[.197]       | 4.25<br>[.167]    | 3.5<br>[.138] | PART<br>NUMBER       |

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|                                                                                                   |  |                                                                                                                        |  |                                                                                                                                                    |  |
|---------------------------------------------------------------------------------------------------|--|------------------------------------------------------------------------------------------------------------------------|--|----------------------------------------------------------------------------------------------------------------------------------------------------|--|
| THIS DRAWING IS A CONTROLLED DOCUMENT.                                                            |  | DWN<br>L VARELA - DCKG5 03-23-00<br>DSE<br>D.L. GORENC 03-23-00<br>APVD<br>M WALMSLEY 03-23-00                         |  | Tyco Electronics Corporation<br>Harrisburg, PA 17105-3608<br> |  |
| DIMENSIONS:<br>mm [INCHES]                                                                        |  | TOLERANCES UNLESS OTHERWISE SPECIFIED:<br>0 PLC ±<br>1 PLC ±<br>2 PLC ± 0.13 [0.005]<br>3 PLC ±<br>4 PLC ±<br>ANGLES ± |  | NAME:<br>PIN HEADER ASSY, 68 POSN,<br>TOP BOARD MTG,<br>SURFACE MT, AMP MEMORY CARD                                                                |  |
|              |  | PRODUCT SPEC<br>APPLICATION SPEC                                                                                       |  | SIZE<br>DATE CODE<br>DRAWING NO<br>RESTRICTED TO                                                                                                   |  |
| MATERIAL<br> |  | FINISH<br>                        |  | WEIGHT<br>6.5 GRAMS<br>CUSTOMER DRAWING<br>A1 00779  5535651  |  |
|                                                                                                   |  |                                                                                                                        |  | SCALE 4:1<br>SHEET 1 OF 1<br>REV A                                                                                                                 |  |